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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/437,580	11/09/1999	ALEXANDER G. MACINNIS	17426US01	8182

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EXAMINER

NGUYEN, KEVIN M

ART UNIT	PAPER NUMBER
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2629

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
3 MONTHS	03/13/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary	Application No. 09/437,580	Applicant(s) MACINNIS ET AL.	
	Examiner Kevin M. Nguyen	Art Unit 2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on RCE filed 1/3/2007.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 51-62 and 71-74 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 51-62 and 71-74 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

Request for Continued Examination

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 01/03/2007 has been entered. An action on the RCE follows:

Response to Arguments

2. This office action is made in response to applicant's amendment/argument filed on 01/03/2007. Claims 51-62 and 71-74 are amended. Applicant's argument, see pages 9-11 with respect to the amended claims 51-62 and 71-74 has been fully considered and are not persuasive. A new ground of rejection is made in view of newly found prior art reference.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 51-62 and 71-74 are rejected under 35 U.S.C. 102(b) as being anticipated by Akiyama et al (US 5,327,157) hereinafter Akiyama.

5. **As to claim 51**, Akiyama teaches a method for horizontally scrolling a display window, the method comprising:

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receiving a window descriptor having a numerical value for indicating how many pixels are to be blanked out at an edge of the display window (*commanding 81 a window identification (a window ID) in which a pixel is defined by the intersection of a row and a column, and commanding 82 to be deleted a number of said pixel when a number of a pixel removed out at a vertical edge of the screen in fig. 6, col. 6, lines 33-38*);

receiving an address of a start of the display window (*commanding 83 for a start address of a partition window 72 in figs. 3 and 6*);

receiving a plurality of graphics data associated with received address, the plurality of graphics data being from the memory (*the command 83 is used to define how the partition window 72 will be mapped into screen buffer 70 in figs. 3 and 6*);

blanking out how many pixels are indicated by the numerical value of the plurality of graphics data (*command 84, e.g., if the horizontal scroll trigger was set to five. Five pixels is defined by the intersection of 5 rows and 5 columns, and commanding 82 to be deleted 5 pixels when 5 pixels removed out at the vertical edge of the screen, col. 6, lines 33-38*); and

displaying the plurality of graphics data such that the blanked out pixels of the plurality of graphics data are not displayed and a first non-blanked pixel of the plurality of graphics data is displayed (*after setting, 5 pixels could not be displayed, and the remaining pixels in the partition window 72 to be displayed on the screen, fig. 6, col. 5, line 36 through col. 6 through col. 7, line 50*).

As to claim 52, Akiyama teaches each partition has a partition window which maps into screen buffer 70. The partition window can be any size from one byte up to the size of its associated partition, col. 3, lines 63-68. Originally a byte was chosen to be a sub multiple of the computer's word size, containing eight bits. The first pixel value "5" is not greater than the second pixel value "12."

As to claim 53, Akiyama teaches one byte containing 8 bits.

As to claim 54, Akiyama teaches the partition window can be any size from one byte up to the size of its associated partition. Four bytes contain 32 bits.

As to claim 71, Akiyama teaches horizontal scroll of the partition window 72 comprising the bottom horizontal edge, col. 6, line 53.

6. **As to claim 55**, Akiyama teaches a method for horizontally scrolling a display window to the left by one or more pixels, the method comprising:

The commands apply for the first window (e.g. a partition window 72) comprising:

receiving a first numerical a value indicating how many pixels are to be blanked out (*commanding 81 a window identification (a window ID) in which a pixel is defined by the intersection of a row and a column, and commanding 82 to be deleted a number of said pixel when a number of a pixel removed out at a vertical edge of the screen in fig. 6, col. 6, lines 33-38*);

receiving a first address of a start of the display window (*commanding 83 for a start address of a partition window 72 in figs. 3 and 6*);

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receiving a first plurality of graphics data associated with the received first address, the first plurality of graphics data being from a memory (*the command 83 is used to define how the partition window 72 will be mapped into screen buffer 70 in figs. 3 and 6*);

blanking out how many pixels are indicated by the first numeric value of the first plurality of graphics data (*command 84, e.g., if the horizontal scroll trigger was set to five. Five pixels is defined by the intersection of 5 rows and 5 columns, and commanding 82 to be deleted 5 pixels when 5 pixels removed out at the vertical edge of the screen, col. 6, lines 33-38*); and

displaying the first of graphics data such that the blanked out pixels of the first plurality of graphics data are not displayed and a first non-blanked pixel of the first plurality of graphics data is displayed (*after setting, 5 pixels could not be displayed, and the remaining pixels in the partition window 72 to be displayed on the screen, fig. 6, col. 5, line 36 through col. 6 through col. 7, line 50*);

The repeat commands apply for the second window (e.g. a partition window 73) comprising:

receiving a second numerical value for indicating how many pixels are to be blanked out (*commanding 81 a window identification (a window ID) in which a pixel is defined by the intersection of a row and a column, and commanding 82 to be deleted a number of said pixel when a number of a pixel removed out at a vertical edge of the screen in fig. 6, col. 6, lines 33-38*);

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receiving a second address of a second start to the display window, the second address pointing to the right of the first start address by one or more graphic memory words (*commanding 85 identifying a second address of the last parameter of the partition window 73 to be out of the screen, col. 7, lines 7-13*); and

receiving a second plurality of graphics data associated with the received second address, the second plurality of graphics data being from the memory (*commanding 86, activating the last parameter of the partition window 73 already mapped to screen buffer 70, col. 7, lines 14-21*);

blanking out how many pixels are indicated by the second numerical value of the second plurality of graphics data (*further commanding 84, e.g., if the number of columns to shift is set to fifteen, the fifteen pixels are out of the screen, col. 6, lines 62-68*); and

displaying the second plurality of graphics data such that the blanked out pixels of the second plurality of graphics data are not displayed and a first non-blanked pixel of the second plurality of graphics data is displayed (*after setting, fifteen pixels could not be displayed, and the remaining pixels of the partition window 73 are displayed on the screen in fig. 6, col. 5, line 36 through col. 6 through col. 7, line 50*).

As to claim 56, Akiyama teaches the first parameter through the last parameter is in the window ID.

As to claim 57, as noting in fig. 3, Akiyama further discloses a first field of a partition window 72 and a second field of a partition window 73 of a plurality of window IDs.

As to claim 58, as noting in fig. 3, Akiyama further discloses a first number of row of a partition window 72 is mapping to corresponding space 72 in buffer 70, and a second number row of the partition window 73 is mapping to corresponding space 73 in buffer 70.

As to claim 72, Akiyama teaches the number of rows and columns for the partition window to be deleted, col. 5, lines 44-48.

As to claim 59 shares similar limitations to those included in claim 55 and therefore the rationale of rejection will be the same. Claim 59 has the added limitation “wherein the second number value is greater than the first numerical value”, whereas Akiyama discloses *the second value “12” is greater than the first value “5”*.

As to claim 60, Akiyama teaches the first number of row and a second number of row are included in the window ID, fig. 3 and 6.

As to claim 61, Akiyama teaches the first field of partition window 72 and the second field of partition window 73, fig. 3.

As to claim 62, Akiyama teaches the first number of row is included in the first window ID 72, and the second number of row is included in the second window ID 73, fig. 3.

As to claim 73, Akiyama teaches the number of columns for the partition window to be deleted, col. 5, lines 49-64.

7. **As to claim 74**, Akiyama teaches a method for horizontally scrolling a display window, the method comprising:

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receiving a window descriptor having a numerical value (*commanding 81 a window identification (a window ID) in which a pixel is defined by the intersection of a row and a column, and commanding 82 to be deleted a number of said pixel when a number of a pixel removed out at a vertical edge of the screen in fig. 6, col. 6, lines 33-38*);

receiving an address of a start of the display window (*commanding 83 for a start address of a partition window 72 in figs. 3 and 6*);

receiving a plurality of graphics data associated with received address, the plurality of graphics data being from the memory (*the command 83 is used to define how the partition window 72 will be mapped into screen buffer 70 in figs. 3 and 6*);

blanking out how many pixels of the plurality of graphics data, said number being equal to the numerical value (*command 84, e.g., if the horizontal scroll trigger was set to five. Five pixels is defined by the intersection of 5 rows and 5 columns, and commanding 82 to be deleted 5 pixels when 5 pixels removed out at the vertical edge of the screen, col. 6, lines 33-38*); and

displaying the plurality of graphics data such that the blanked out pixels of the plurality of graphics data are not displayed and a first non-blanked pixel of the plurality of graphics data is displayed (*after setting, 5 pixels could not be displayed, and the remaining pixels in the partition window 72 to be displayed on the screen, fig. 6, col. 5, line 36 through col. 6 through col. 7, line 50*).

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Response to Arguments


8. Applicant's arguments with respect to claims 51-62 and 71-74 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to KEVIN M. NGUYEN whose telephone number is 571-272-7697. The examiner can normally be reached on MON-THU from 8:00-6:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, a supervisor RICHARD A. HJERPE can be reached on 571-272-7691. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8000.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the Patent Application Information Retrieval system, see <http://portal.uspto.gov/external/portal/pair>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


Kevin M. Nguyen
Patent Examiner
Art Unit 2629

KMN
March 7, 2007